

FIG. 1

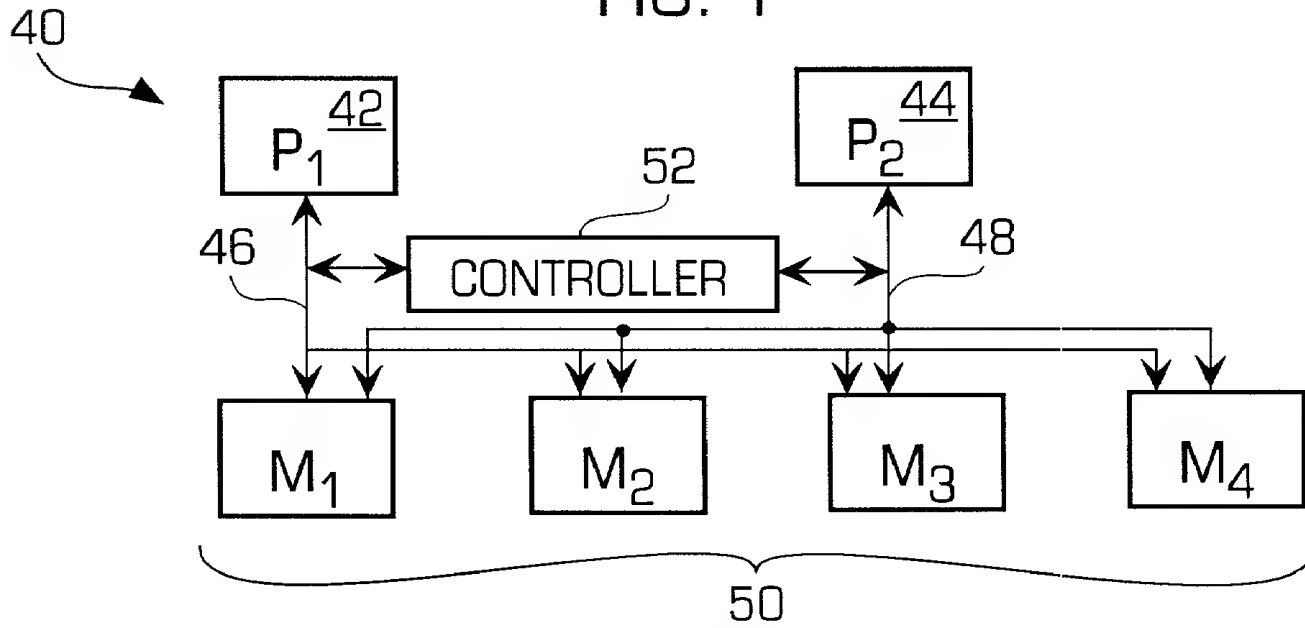


FIG. 2

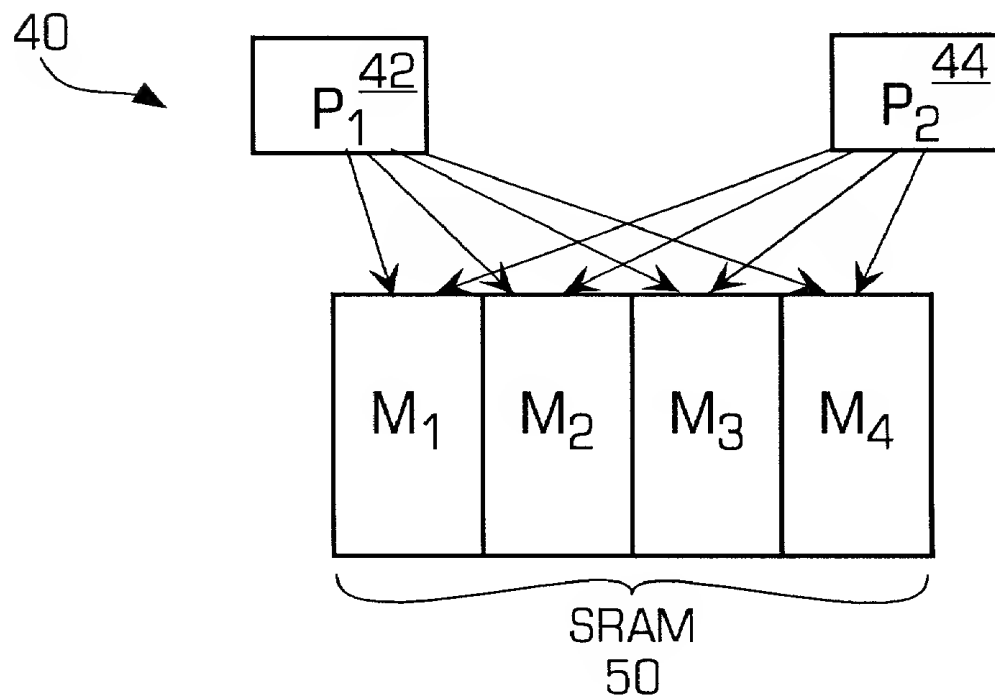


FIG. 3

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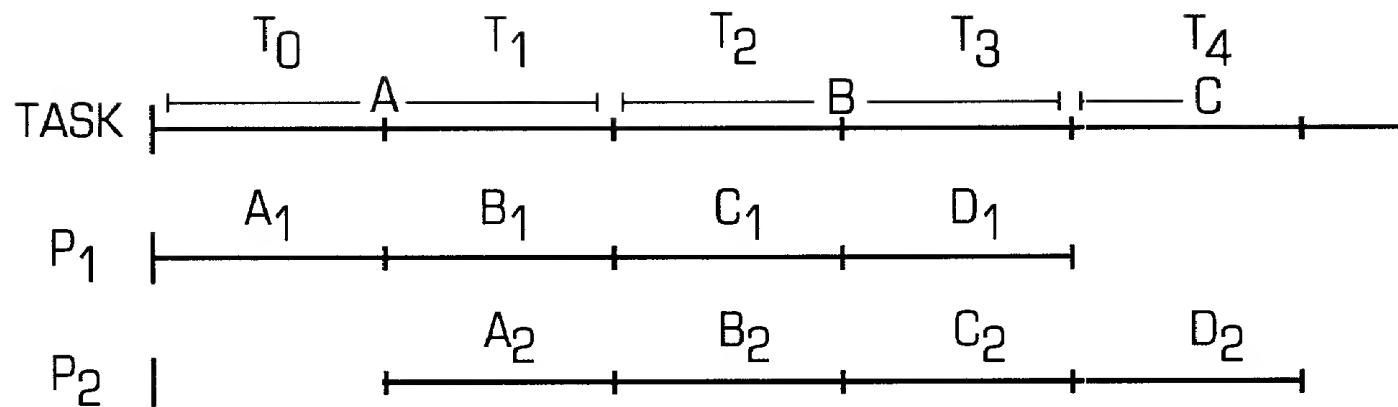


FIG. 4

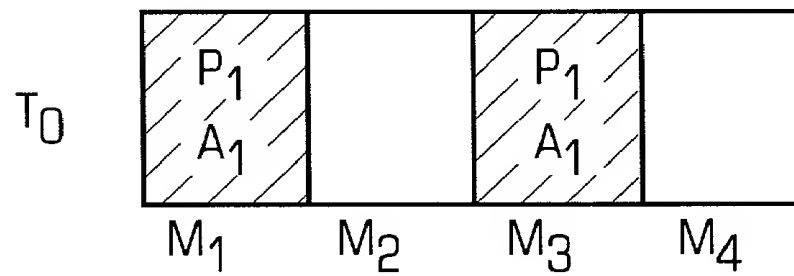


FIG. 5A

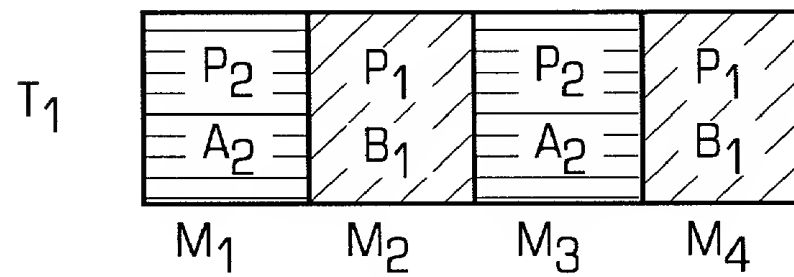


FIG. 5B

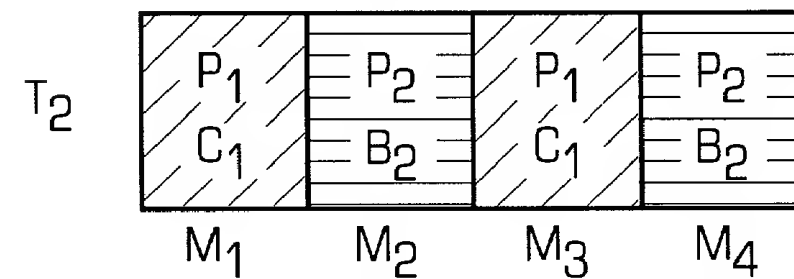


FIG. 5C

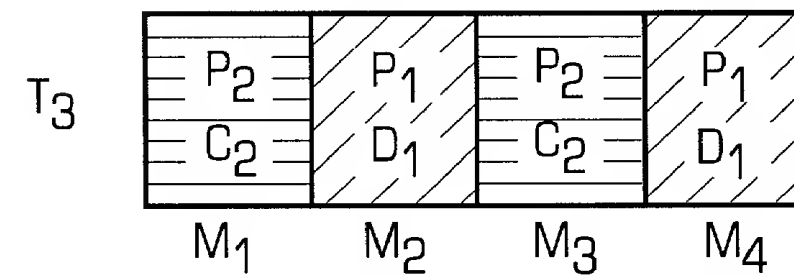


FIG. 5D

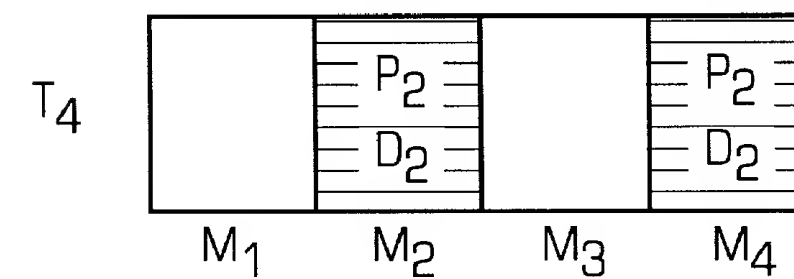
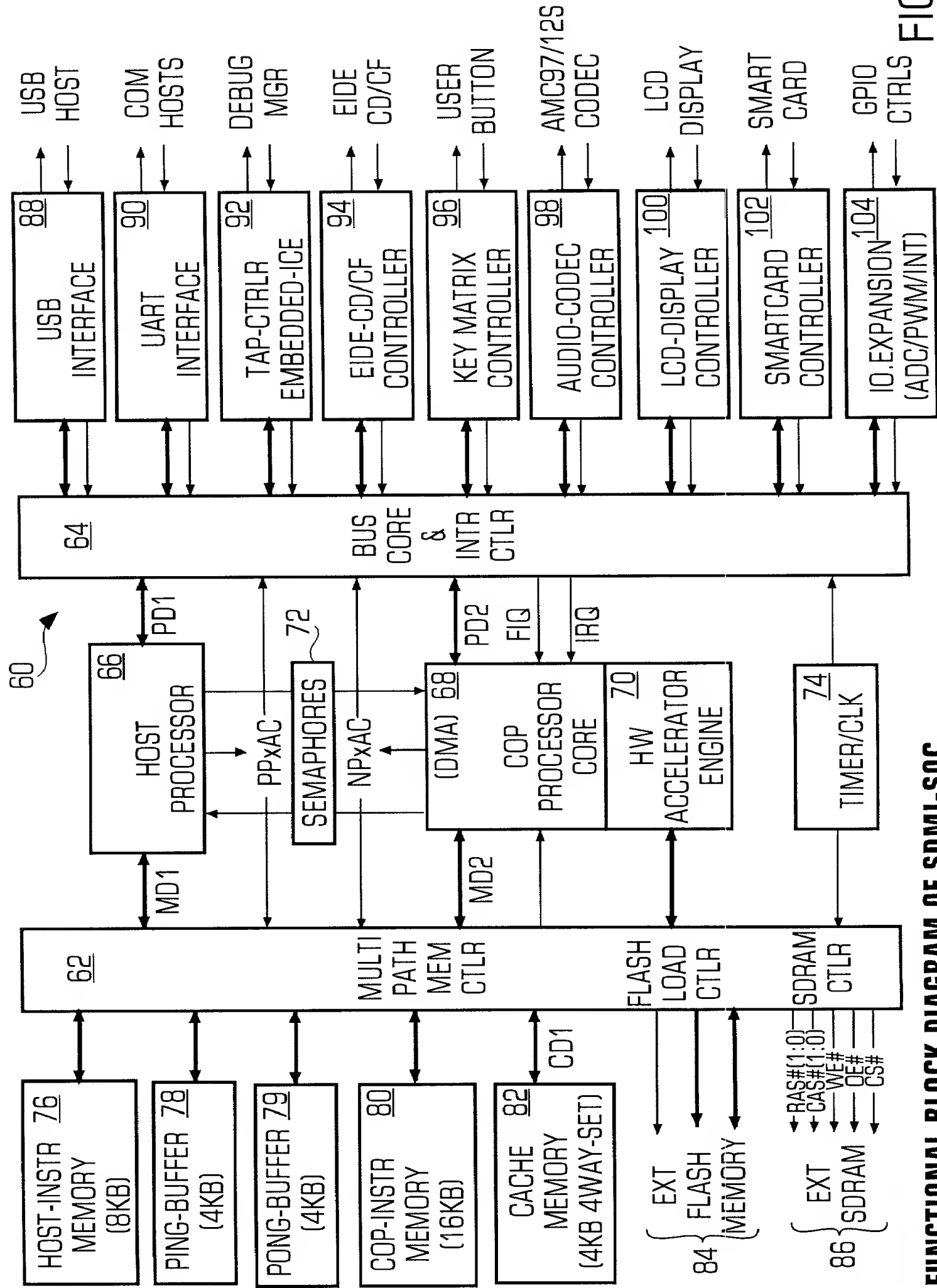


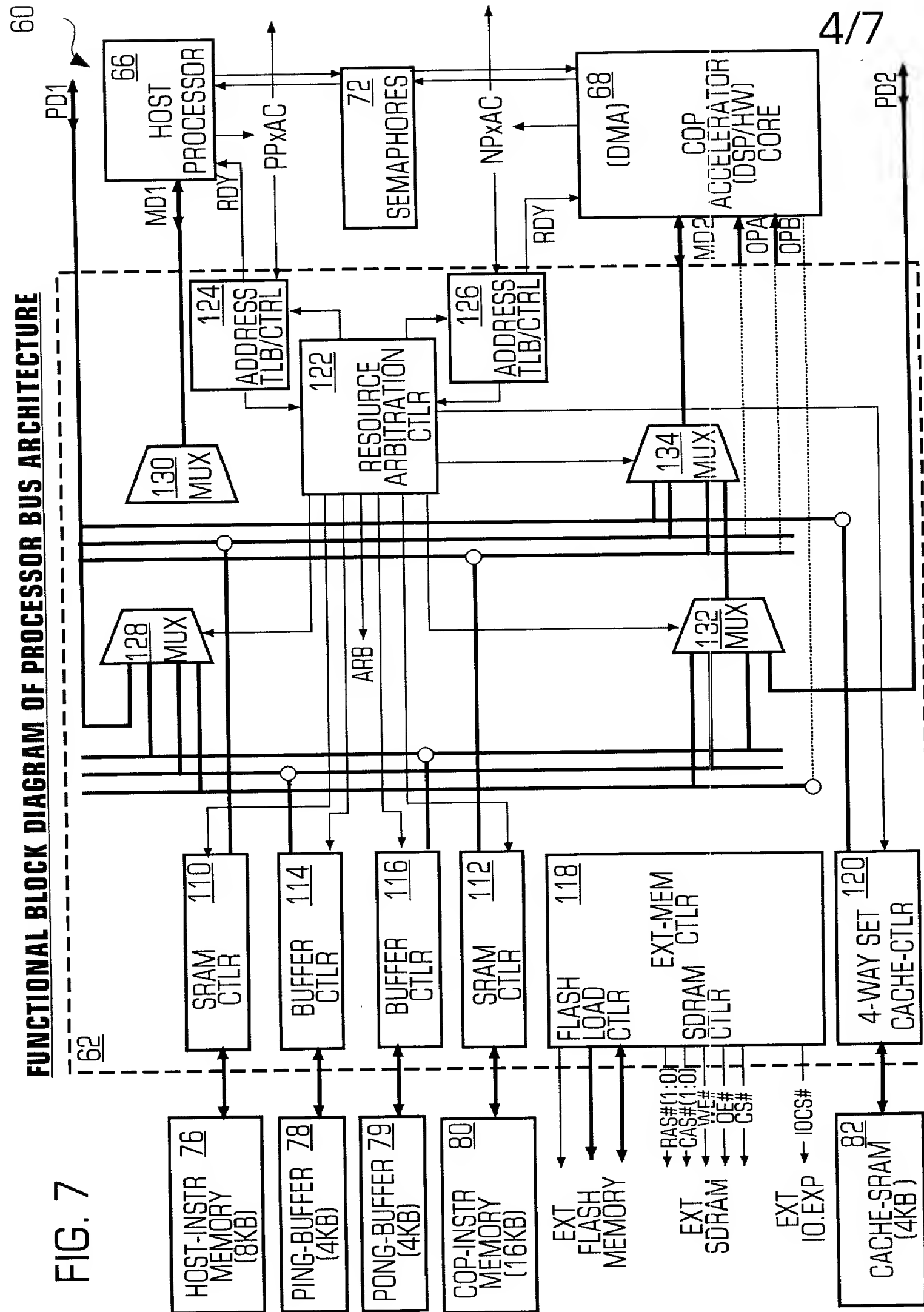
FIG. 5E



FUNCTIONAL BLOCK DIAGRAM OF SDMI-SOC

FIG. 6

FUNCTIONAL BLOCK DIAGRAM OF PROCESSOR BUS ARCHITECTURE



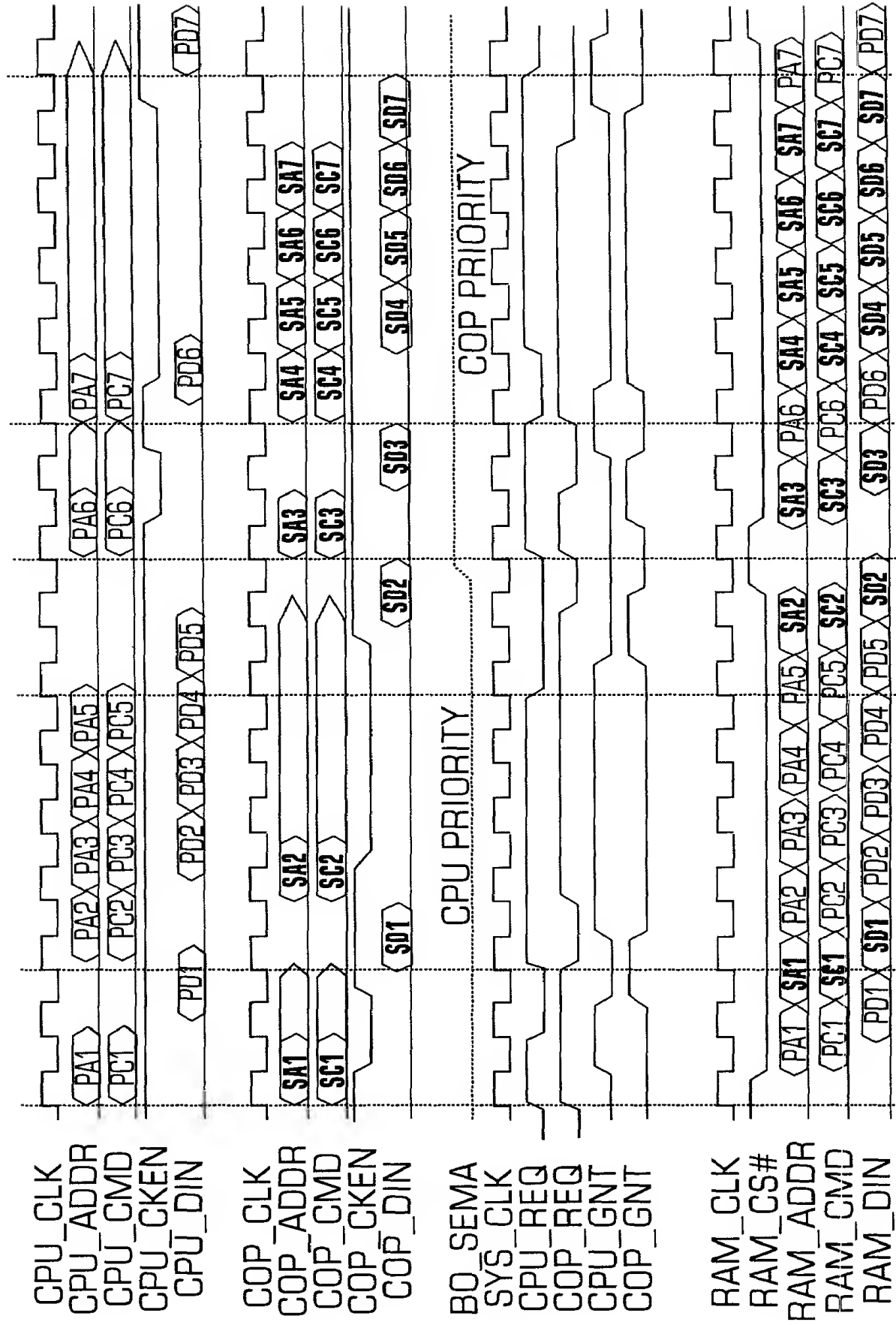


FIG. 8

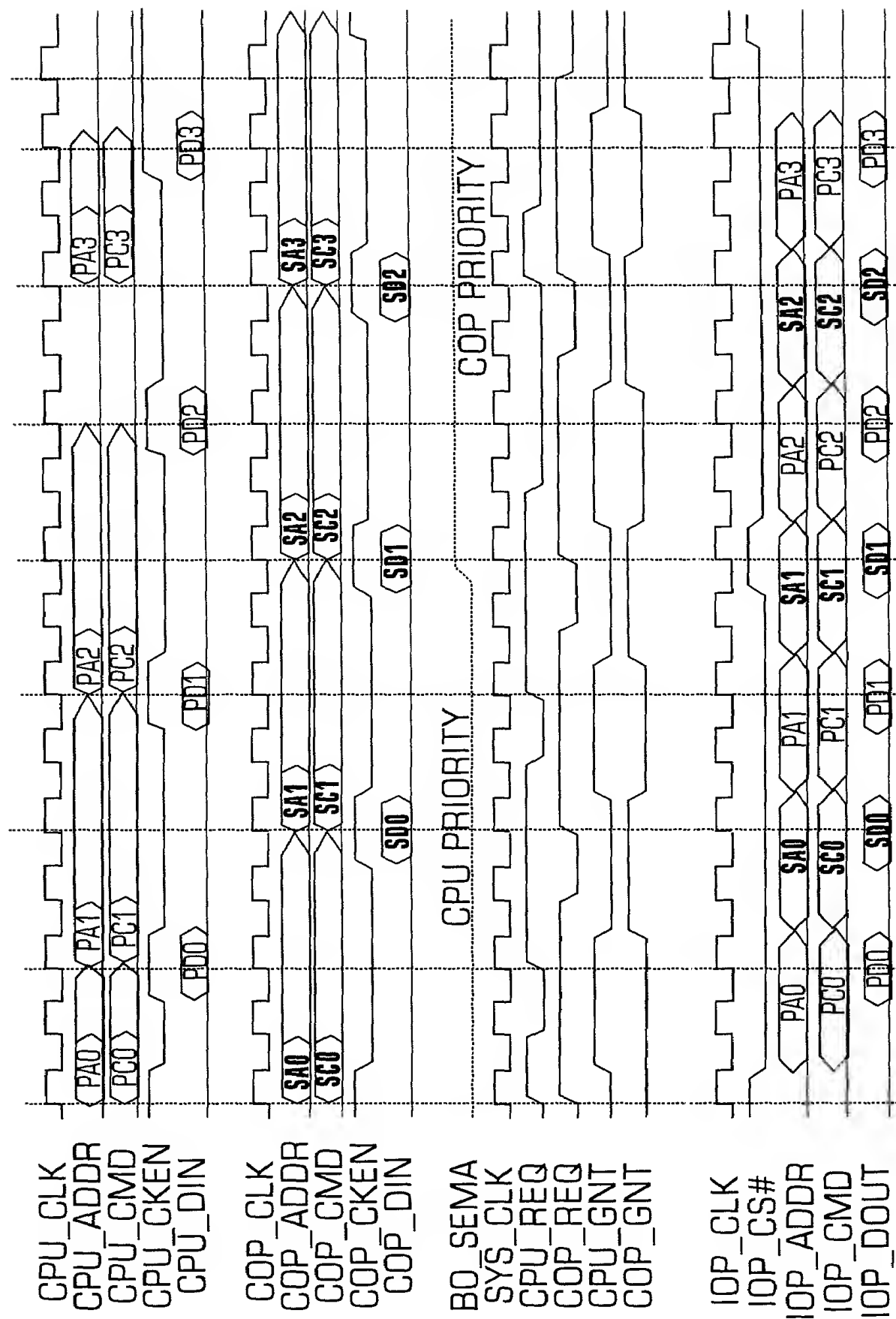


FIG. 10